

1 Set Timer

Hardware Interruption Circuitry

3 EXT Exception (Timer POP)

Registers

Current PSW

CPU

4

Hardware

OS

Fixed Storage Locations

OLD EXT PSW

NEW EXT PSW

5

5

6

First-Level Interrupt Handlers

Ext

I/O

PGM

SVC

7

Second-Level Interrupt Handler
and dispatcher code

Supervisor State

Problem State

APPLICATIONS

1

Current running
process

2

8

Next to run
process

